

	Class	Subclass
ISSUE CLASSIFICATION		

**PATENT NUMBER**

**U.S. UTILITY Patent Application**

FA O.I.P.E.  
IK SB0151  
SCANNED 102 a.m. Am

PATENT DATE

APPLICATION NO. AS 09/82-1478	CONT/PRIOR 05/11/82	CLASS 430	SUBCLASS 311	ART UNIT 1756	EXAMINER Sagar DUSA
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APPLICANTS  
Hung Ng

TITLE  
Method for polysilicon conductor (PC) Trimming for shrinking critical dimension and isolated-nested offset correction

PTO-2040  
12/88

**ISSUING CLASSIFICATION**

ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
<b>INTERNATIONAL CLASSIFICATION</b>												

☐ Continued on Issue Slip Inside File Jacket

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3 Sheets of Formal Drawings filed 5-30-01

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> Sheets Drwg.      Figs. Drwg.      Print Fig.		<b>CLAIMS ALLOWED</b> Total Claims      Print Claim for O.G.	
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Assistant Examiner)      (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner)      (Date)		<b>ISSUE FEE</b> Amount Due      Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)      (Date)		<b>ISSUE BATCH NUMBER</b>	

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